RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/647,838 Filing Date: August 25, 2003

Title: HARDWARE/SOFTWARE LOCK FOR SECURE LINKAGE BETWEEN ALGORITHMIC DECISION PROCESS AND CRITICAL

SYSTEM FUNCTION EXECUTION

REMARKS

This responds to the Office Action dated January 5, 2006. No claims are amended, added, or canceled. Claims 1-26 remain pending in this application.

Double Patenting Rejection

Claims 1-26 were rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-28 of U.S. Patent No. 6,612,994.

Applicant submits herewith a Terminal Disclaimer to obviate the rejection. Applicant respectfully requests reconsideration and allowance of the claims.

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CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 373-6960 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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By their Representatives,

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Date 1-30-06

Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Comprissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this day of January, 2006.

Name

Signature